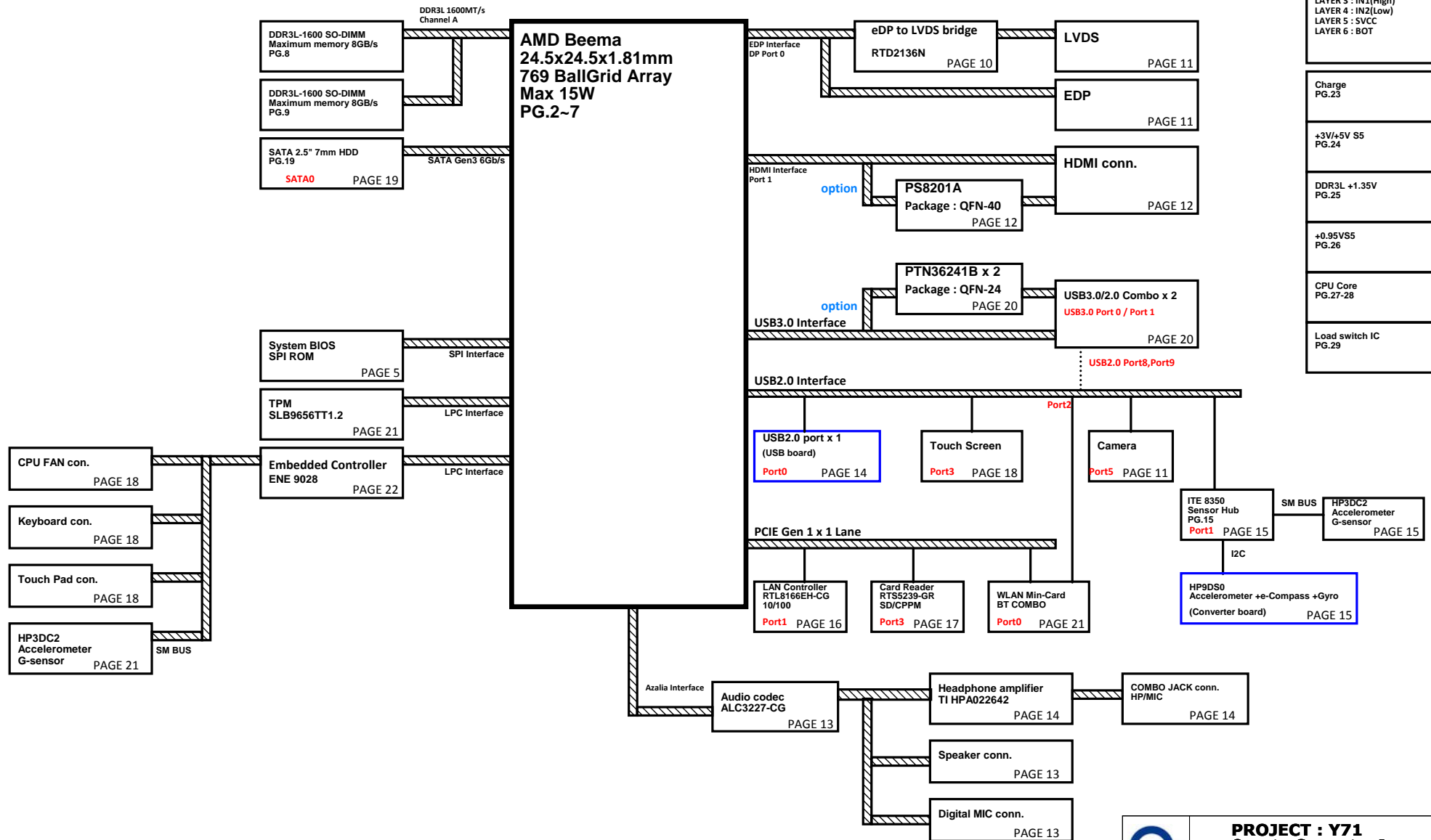



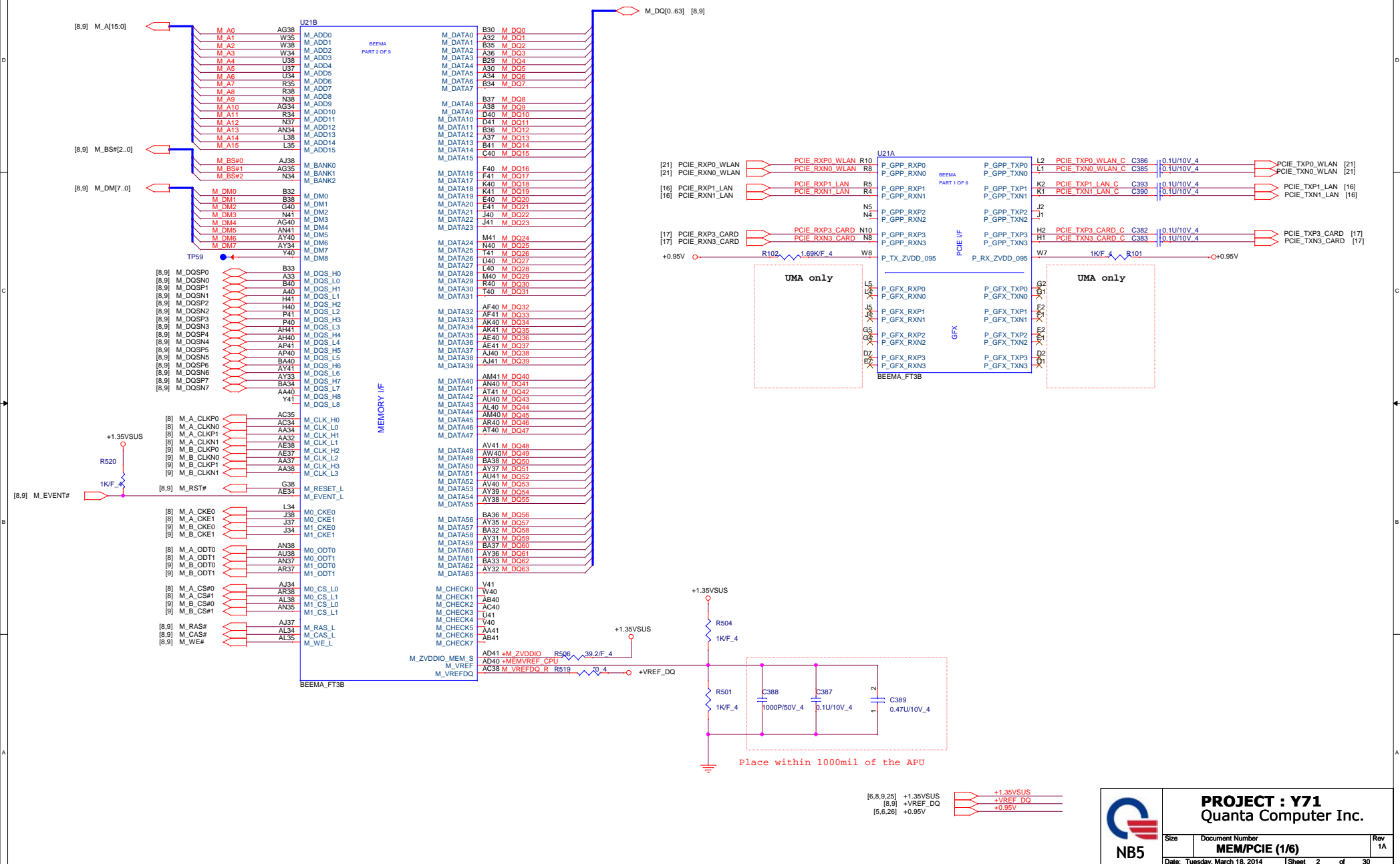
Y72 (HP Napa) AMD Beema Platform Block Diagram

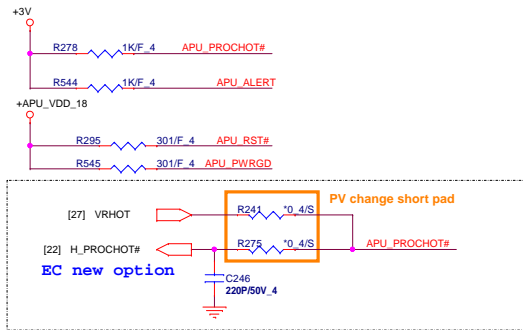
01



www.schematic-x.blogspot.com

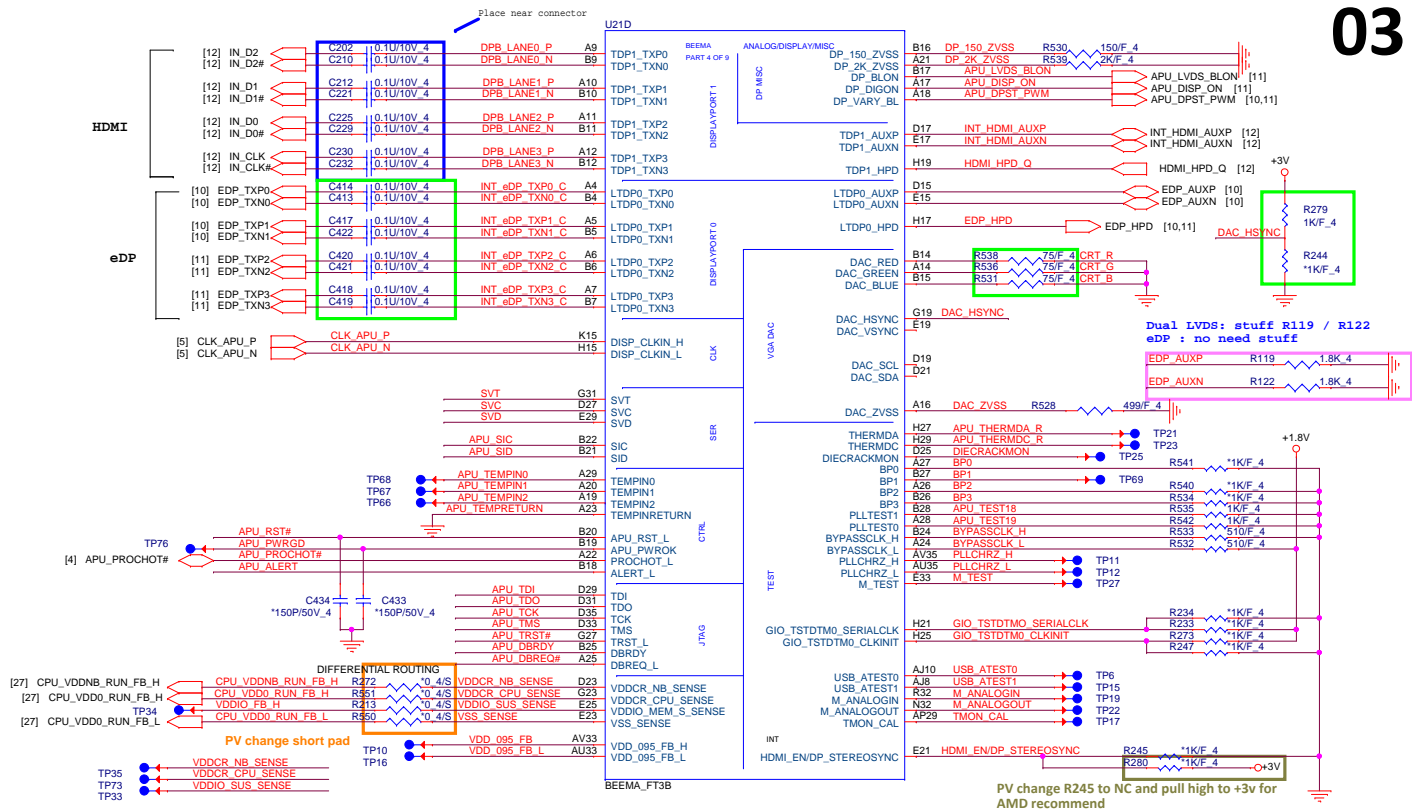
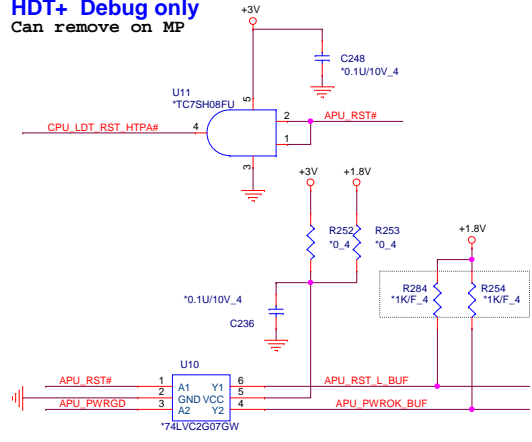
| | | | |
|---|-------------------------------|--|--------|
|  | | PROJECT : Y71 Quanta Computer Inc. | |
| | | Document Number BLOCK DIAGRAM | |
| Size Custom | Date: Tuesday, March 18, 2014 | Sheet 1 of 30 | Rev 2A |



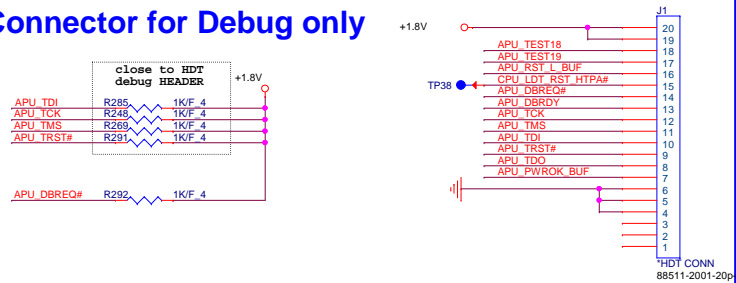


HDT+ Debug only

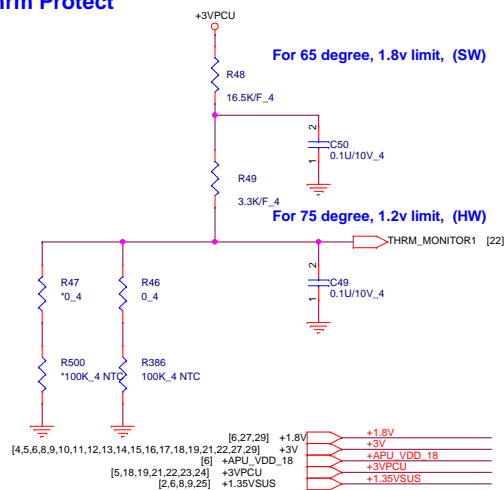
Can remove on MP



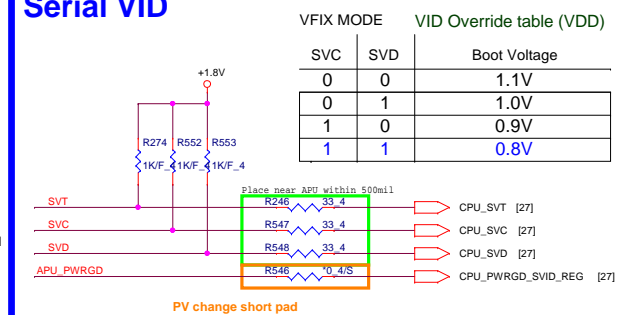
HDT+ Connector for Debug only



IO Thrm Protect

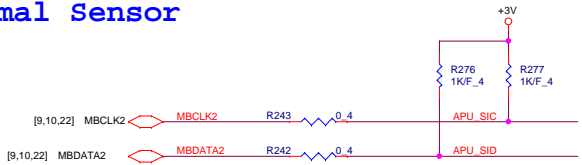


Serial VID



| VFIX MODE | | VID Override table (VDD) | |
|-----------|-----|--------------------------|--|
| SVC | SVD | Boot Voltage | |
| 0 | 0 | 1.1V | |
| 0 | 1 | 1.0V | |
| 1 | 0 | 0.9V | |
| 1 | 1 | 0.8V | |

Thermal Sensor



PROJECT : Y71
Quanta Computer Inc.

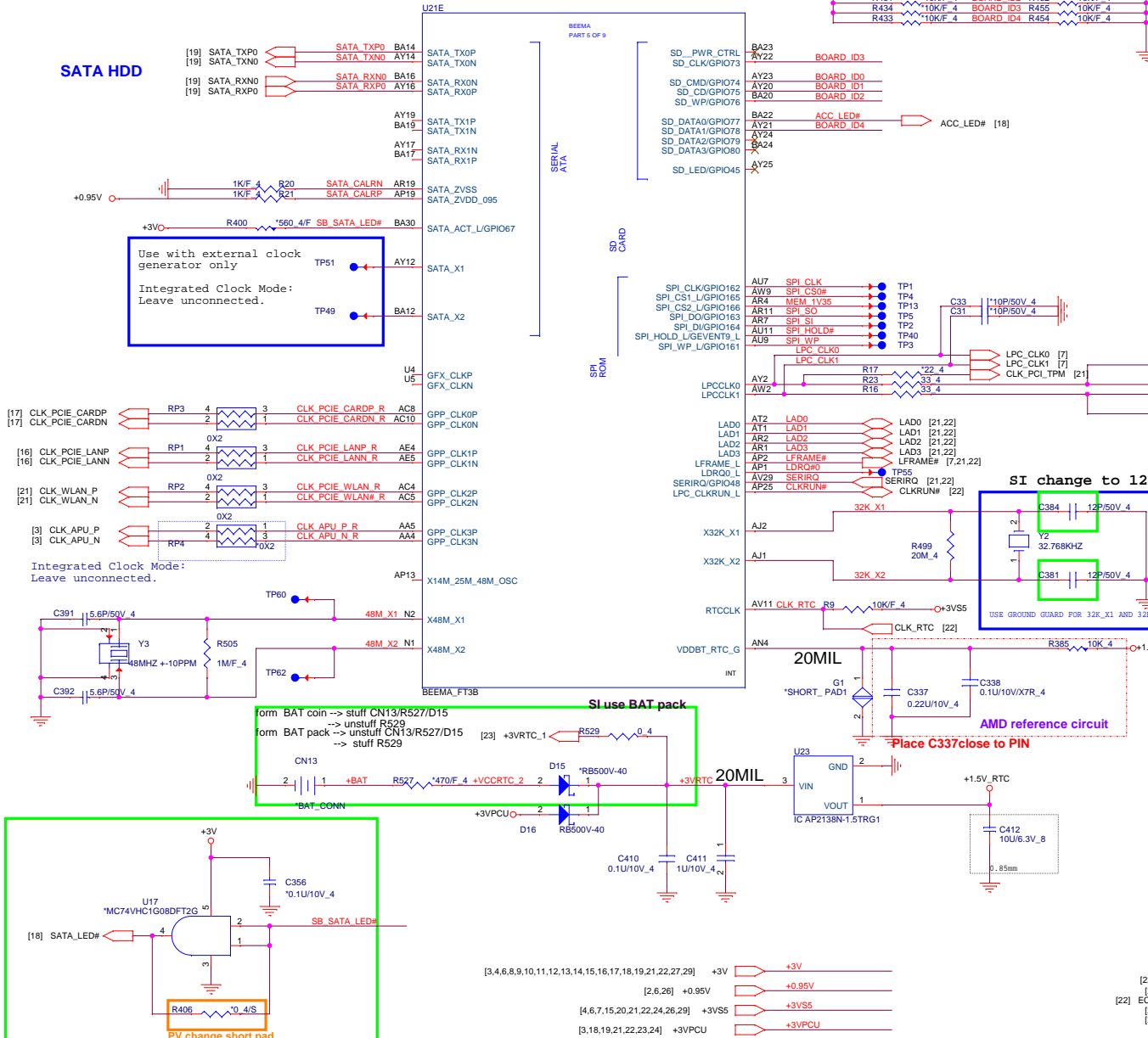
| Size | Document Number | Rev |
|---|---------------------|-----------|
| | DIS/MI (2/6) | 1A |
| Date: Tuesday, March 18, 2014 Sheet 3 of 30 | | |



BOARD ID SETTING

| Model | BOARD_ID0 | BOARD_ID1 | BOARD_ID2 | BOARD_ID3 | BOARD_ID4 |
|-------------|-----------|-----------|-----------|-----------|-----------|
| clamshell | 0 | 0 | 0 | 0 | 0 |
| convertible | 1 | 0 | 0 | 0 | 0 |
| | 0 | 1 | 0 | 0 | 0 |
| | 1 | 1 | 0 | 0 | 0 |
| | 0 | 0 | 1 | 0 | 0 |
| | 1 | 0 | 1 | 0 | 0 |

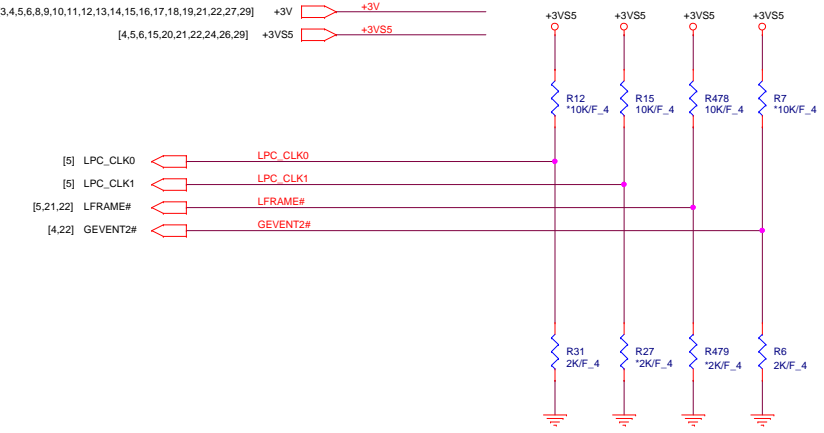
SATA HDD





STRAPS PINS

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

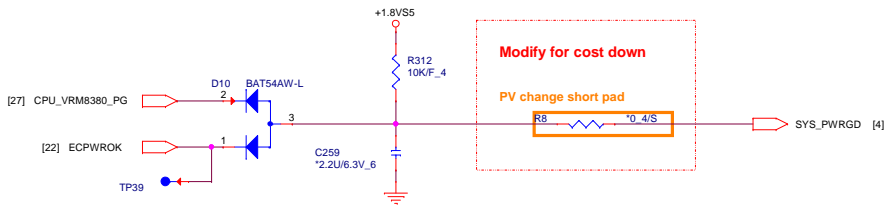


REQUIRED STRAPS

| | | | | | LPC_CLK0 | LPC_CLK1 | LFRAME# | GEVENT2# |
|-----------|--|--|--|--|-------------------------------------|---------------------------|--------------------|-------------------------|
| PULL HIGH | | | | | BOOT FAIL TIMER ENABLED | CLKGEN ENABLED DEFAULT | SPI ROM DEFAULT | 1.8V SPI ROM |
| PULL LOW | | | | | BOOT FAIL TIMER DISABLED DEFAULT | CLKGEN DISABLED | LPC ROM | 3.3V SPI ROM DEFAULT |

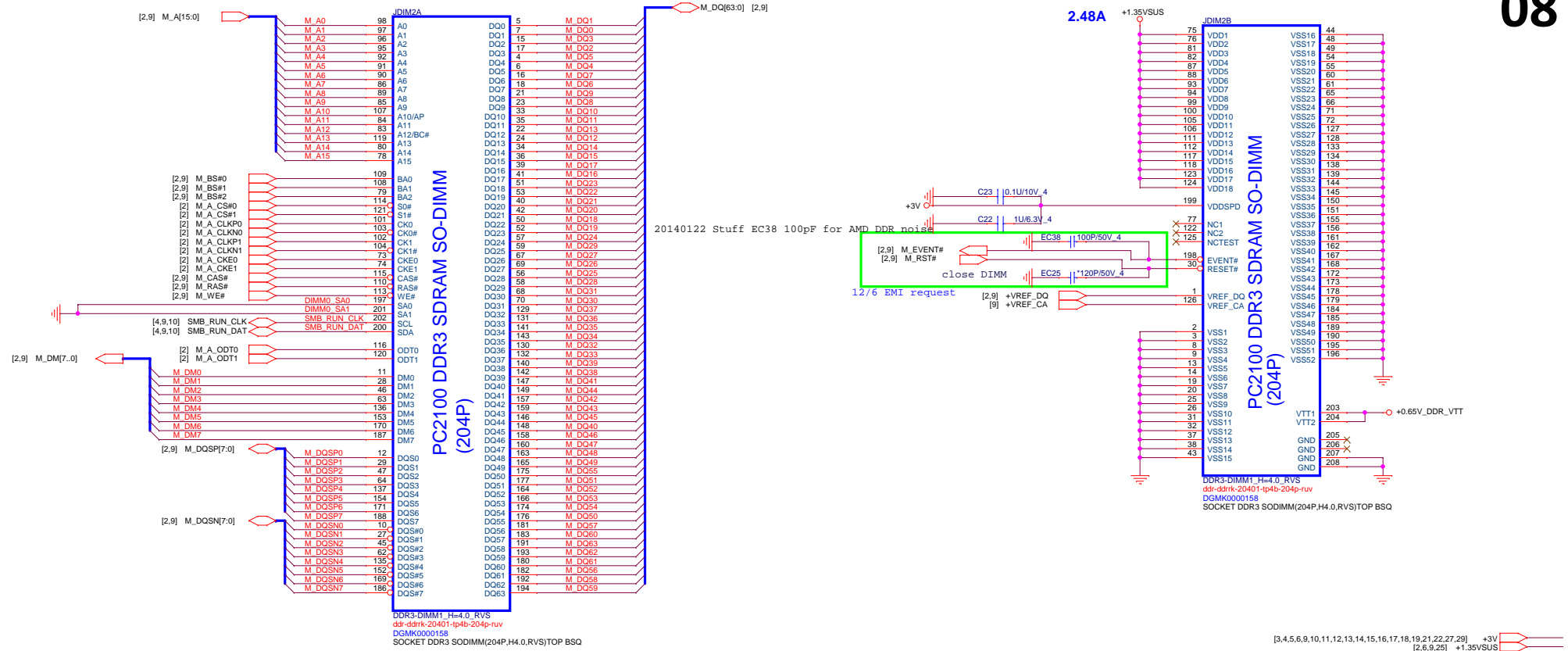
DEBUG STRAPS

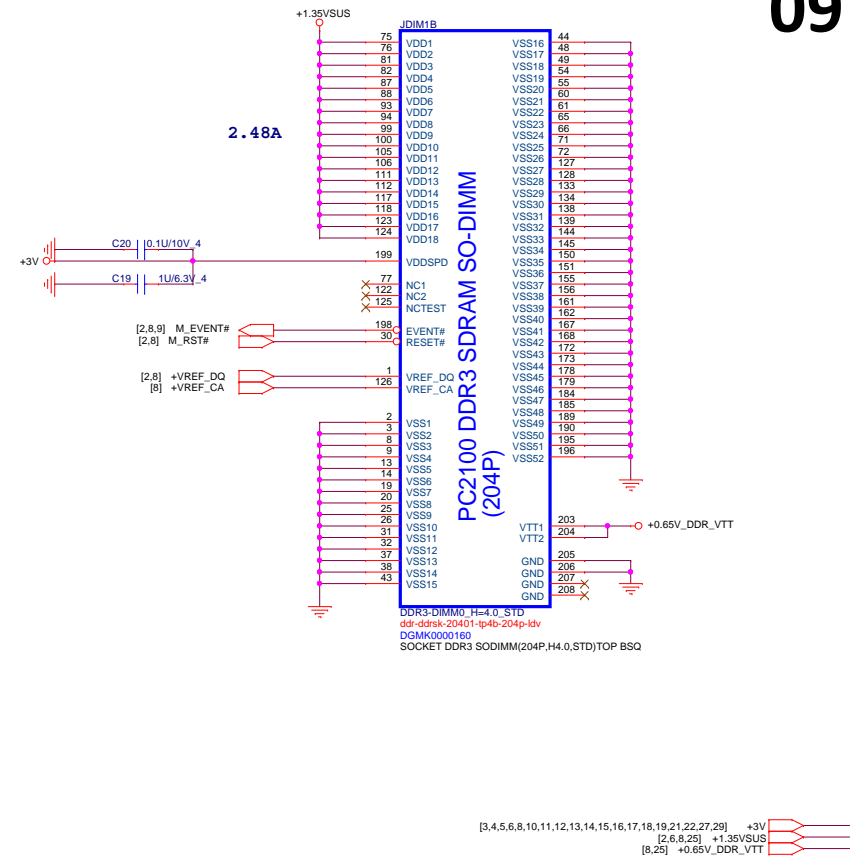
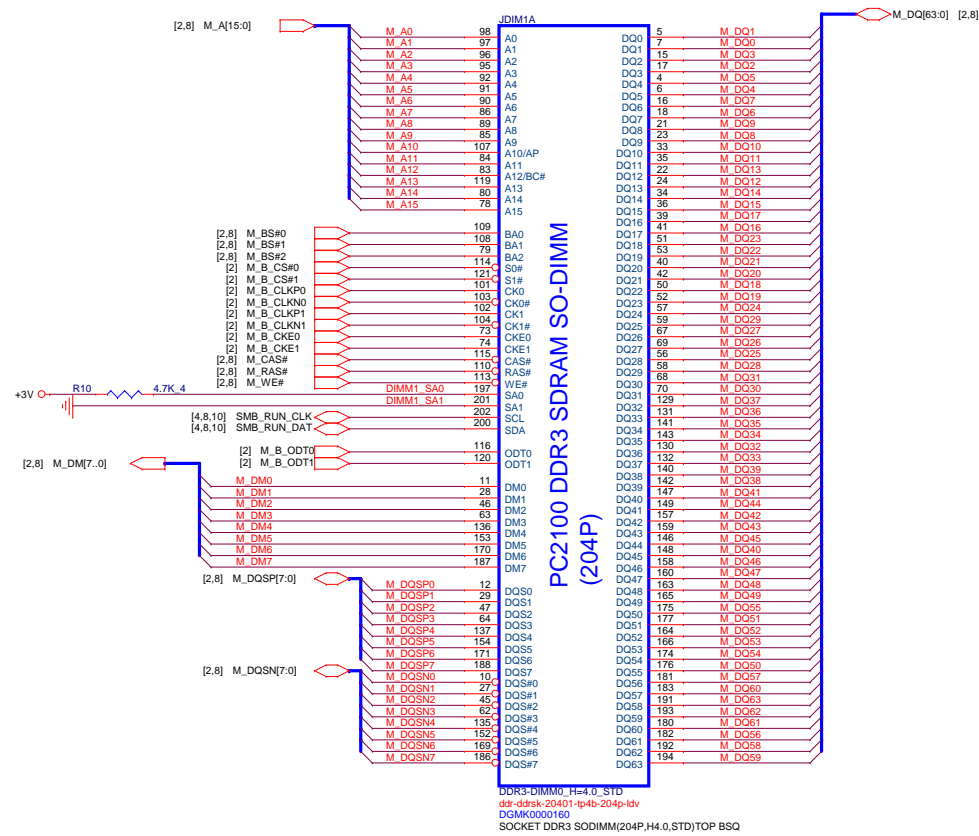
SYS_PWRGD



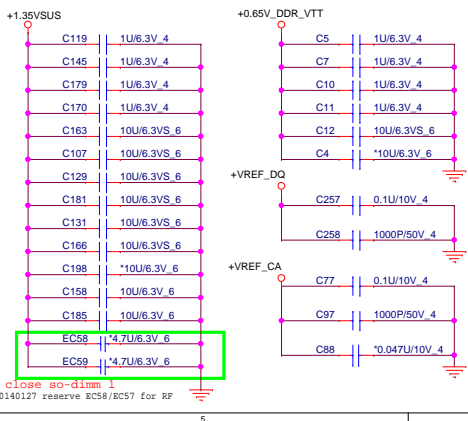
PROJECT : Y71
Quanta Computer Inc.

| | | |
|-------------------------------|--------------------|-----|
| Size | Document Number | Rev |
| | STRAP (6/6) | 1A |
| Date: Tuesday, March 18, 2014 | Sheet 7 of 30 | |



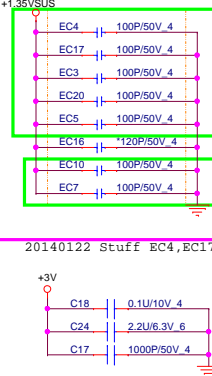


Place these Caps near So-Dimm1.



For EMI RESERVE

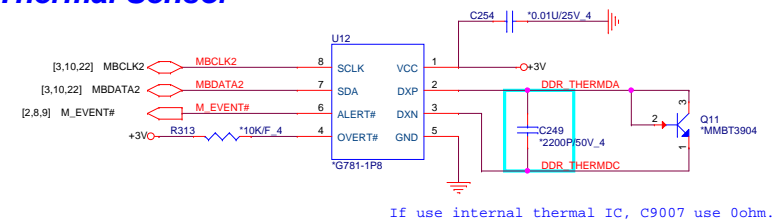
6/21/2012 for EMI



20140122 Stuff EC4,EC17,EC3,EC20,EC5,EC10,EC7 100pF for AMD DDR noise

Local Thermal Sensor

DDR3 Thermal Sensor



If use internal thermal IC, C9007 use 0ohm.

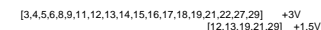
Main:AL000781039 G781-1P8(9Ah)
 2nd:AL001412005 EMC1412-2-ACZL-TR(9Ah)

Main:AL001412003 EMC1412-1-ACZL-TR(9Ah)
 2nd:AL000431014 TMP431ADGKR(9Ah)



PROJECT : Y71
Quanta Computer Inc.

| Size | Document Number | Rev |
|-------------------------------|--------------------------|-----|
| Custom | System Memory 2/2 (4.0H) | 1A |
| Date: Tuesday, March 18, 2014 | Sheet 9 of 30 | |



PROJECT : Y71
Quanta Computer Inc.

| | | |
|-------------------------------|--|------------------|
| Size Custom | Document Number LVDS converter RTD2136 | Rev 1A |
| Date: Tuesday, March 18, 2014 | | Sheet 10 of 30 |

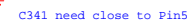


Pine 15/17: keep 20 Mile Trace

Pine 18: keep 80 Mile Trace

IC body need add 3x3 via to GND

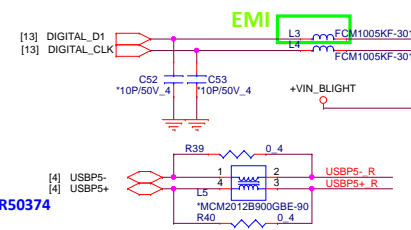
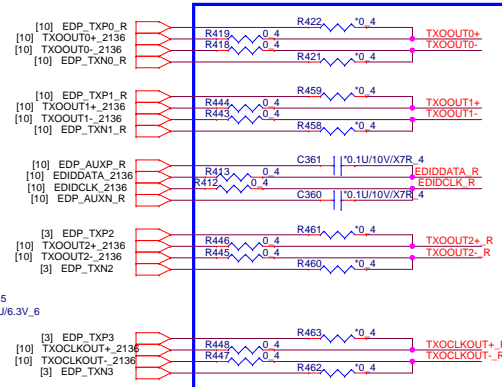
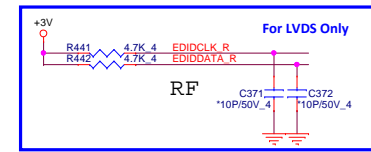
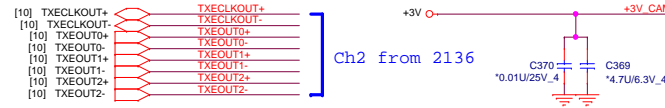
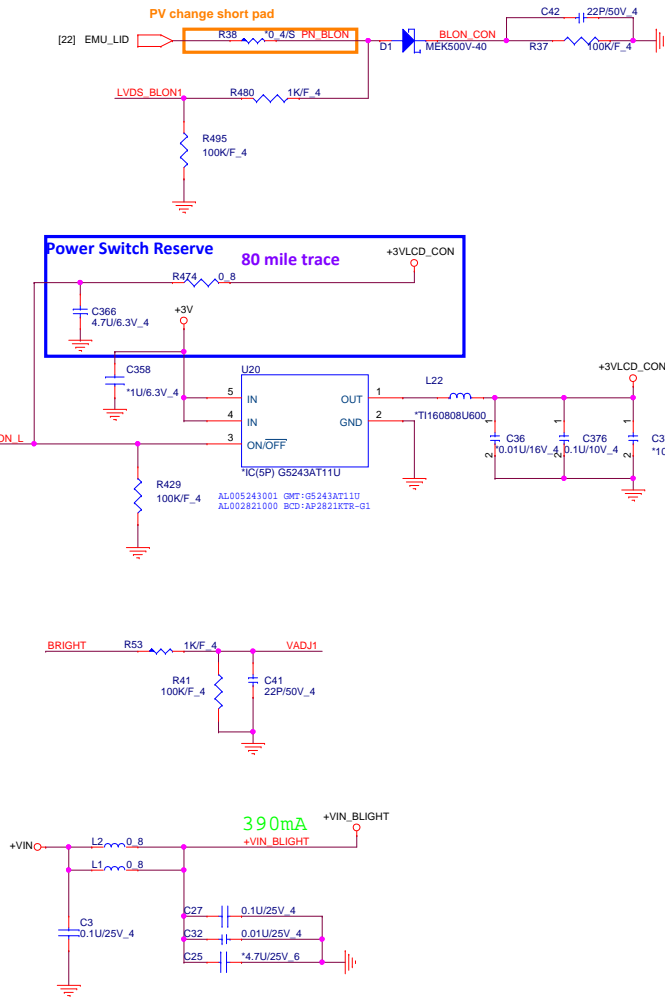
| | | MODE_CFG0(PIN47) | |
|------------------|---|------------------|-------------|
| | | 0 | 1 |
| MODE_CFG1(PIN48) | 0 | X | EP MODE |
| | 1 | ROM ONLY MODE | EEPROM MODE |



C363 close Pin:

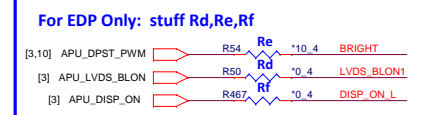
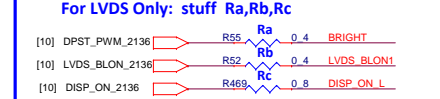
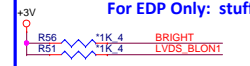
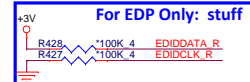
LVDS conn.

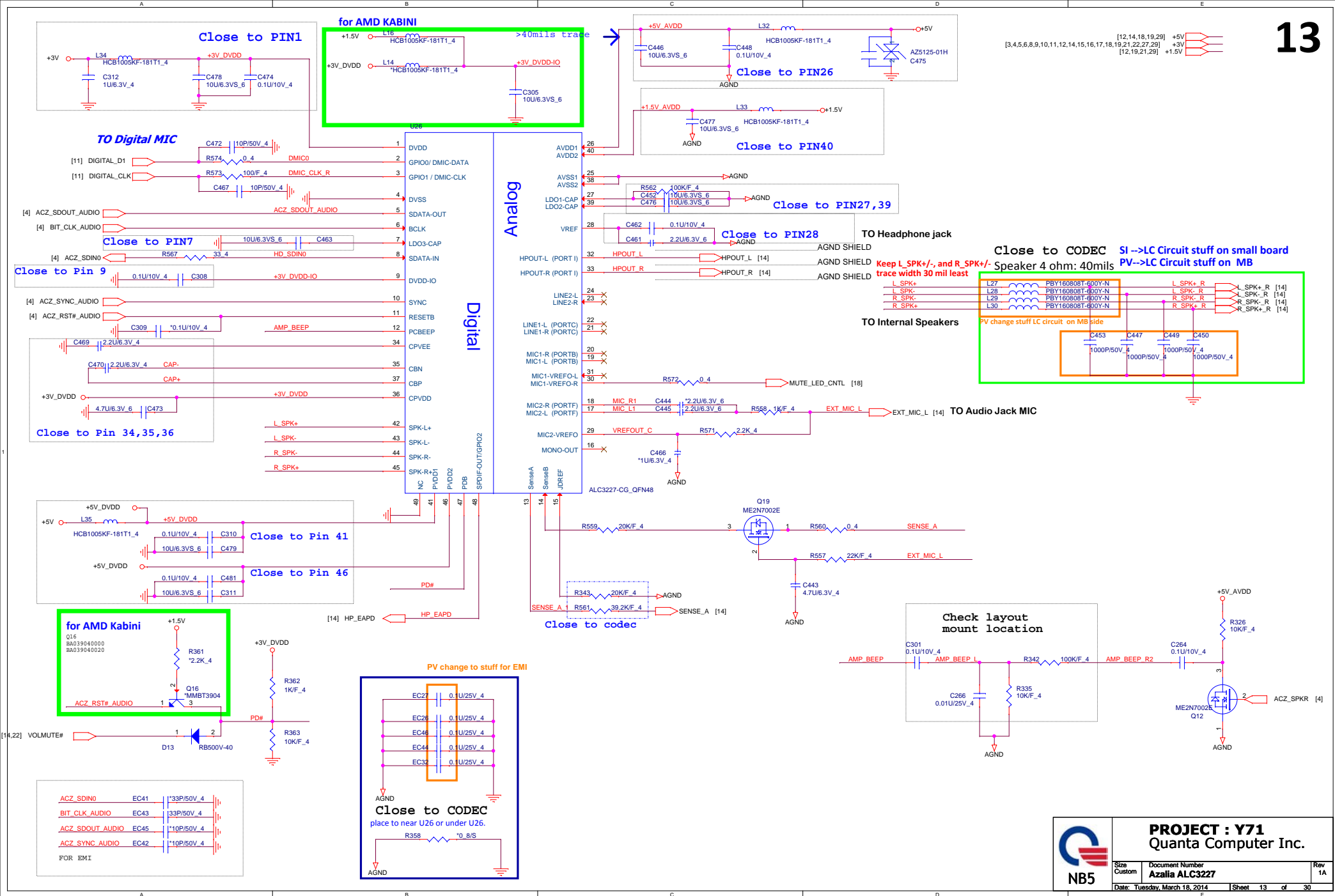
11

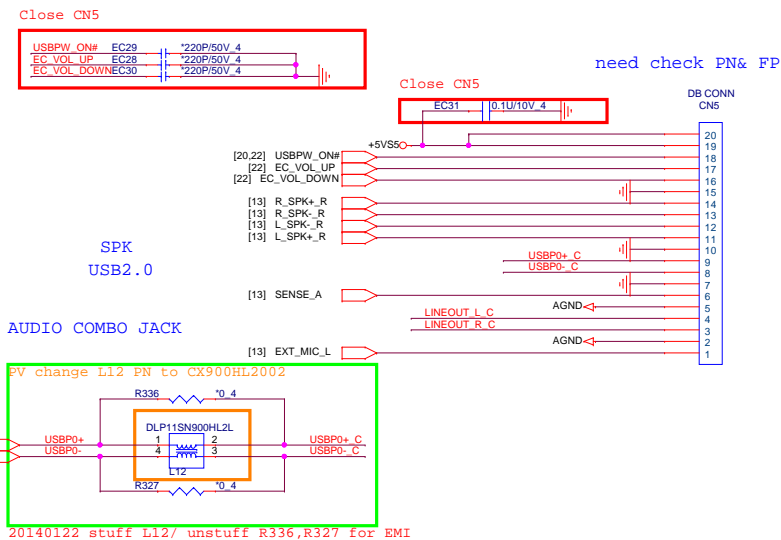


For EDP Only: stuff
R37/R45/R72/R73/C30120/C30121/R50368/R50370/R50372/R50374

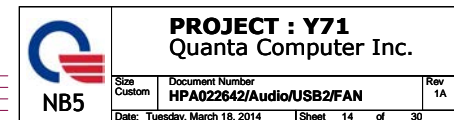
For LVDS only: stuff
R17/R18/R11/R120/R33/R34/R50367/R50369/R50372/R50374

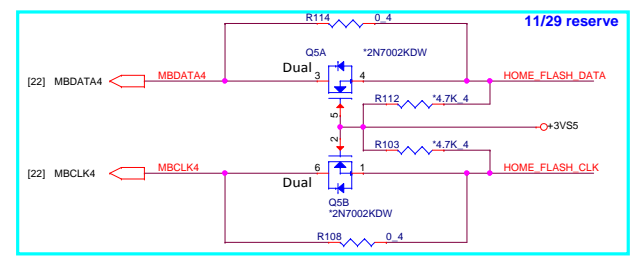
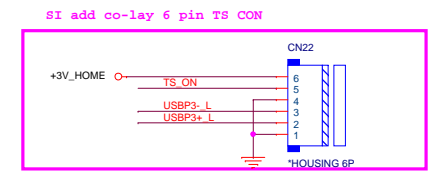
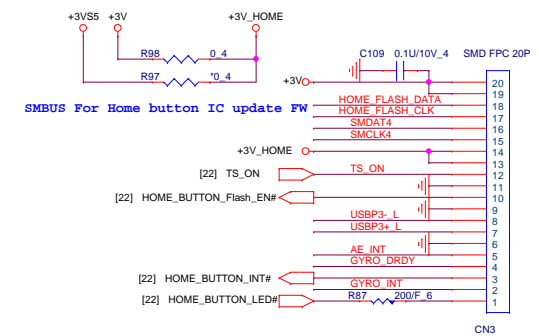
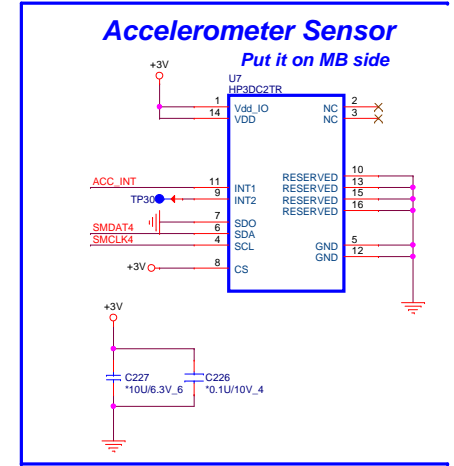
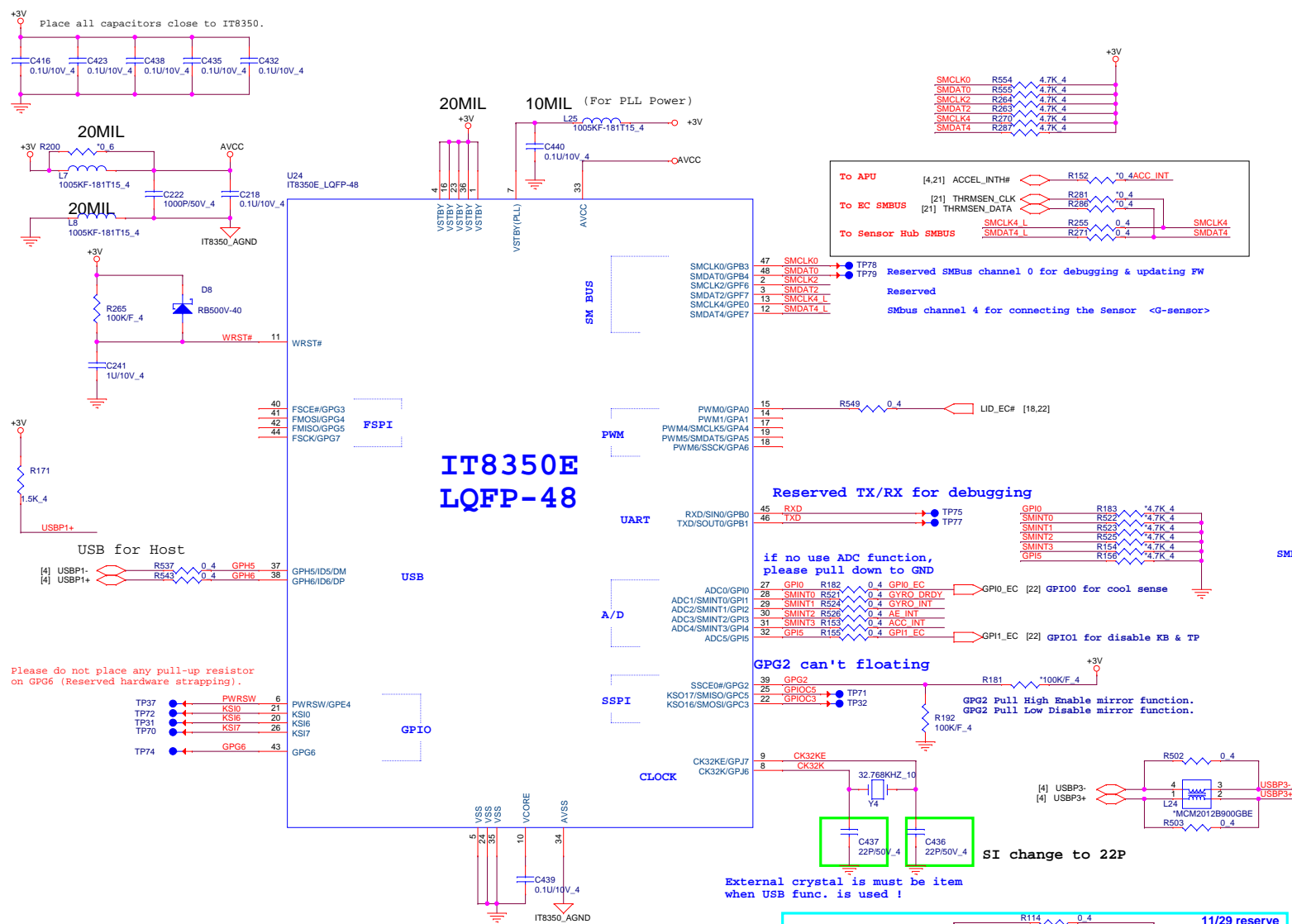






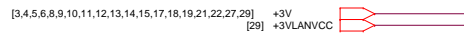
[20,24,25,26,27,28,29] +5VS5
[3,4,5,6,8,9,10,11,12,13,15,16,17,18,19,21,22,27,29] +3V
[12,13,18,19,29] +5V
[3,5,18,19,21,22,23,24] +3VPCU

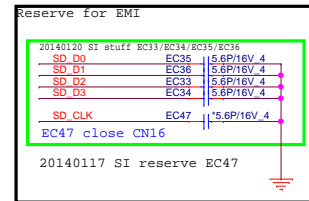
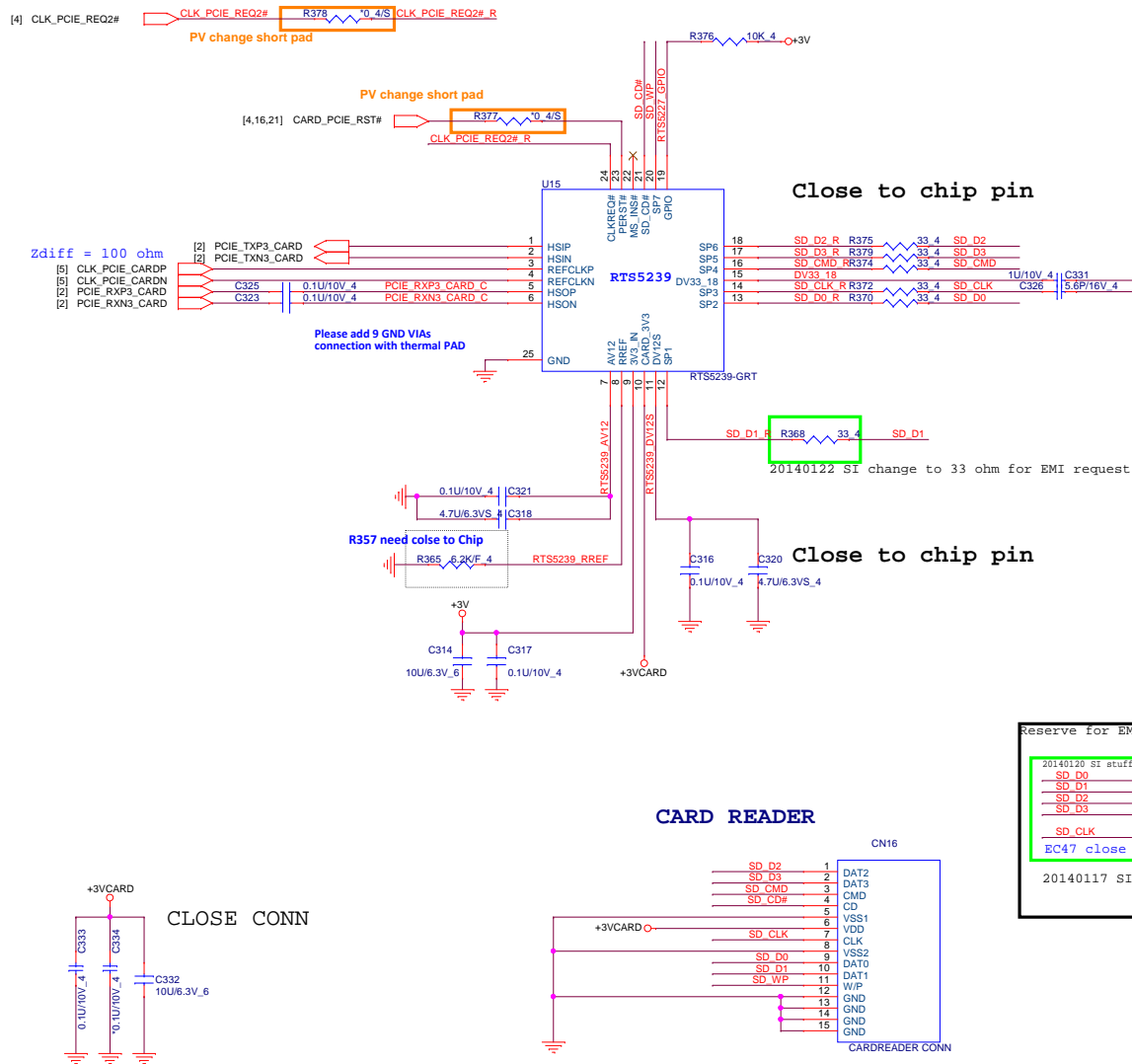




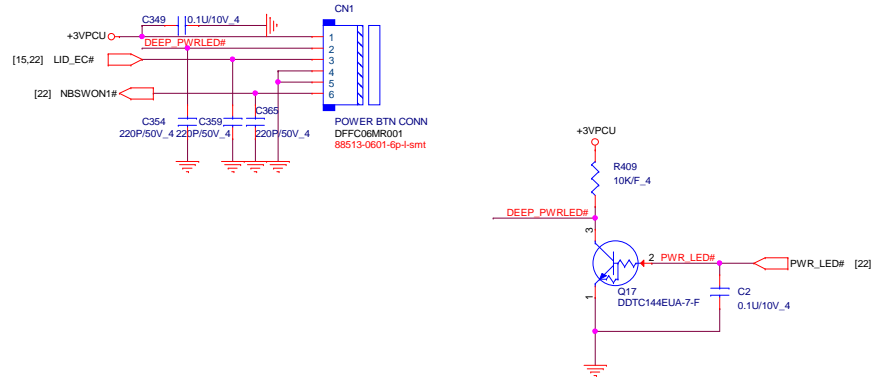
PROJECT : Y71
Quanta Computer Inc.

| | | |
|--|---------------------------|-----|
| Size | Document Number | Rev |
| Custom | Sensor Hub 8350/HP3DC2/TS | 1A |
| Date: Tuesday, March 18, 2014 Sheet 15 of 29 | | |

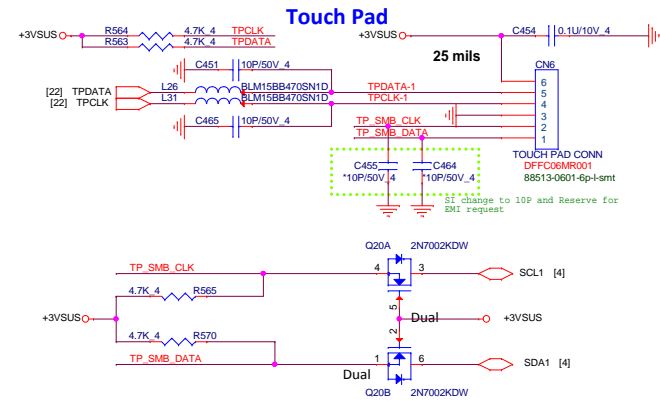




Power Button Connector

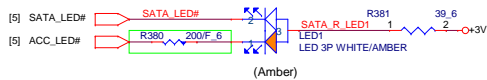


Touch Pad

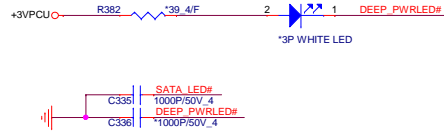


LED

SATA_LED

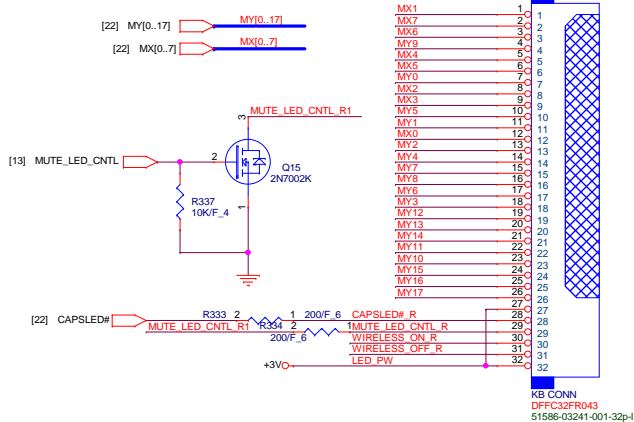


PWR_LED



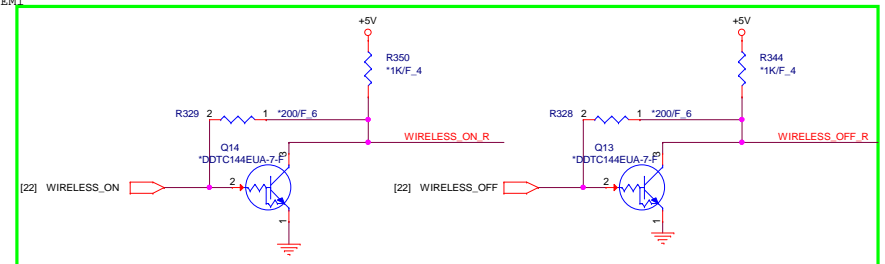
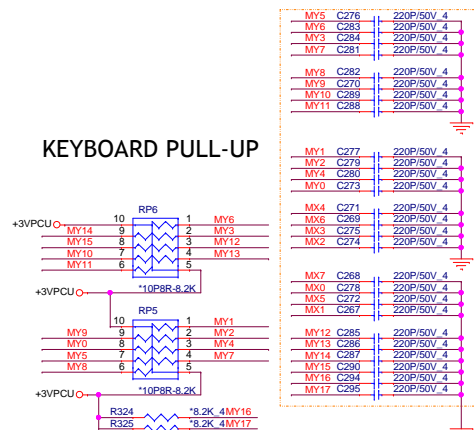
PWR LED Clamshell only

KEYBOARD Con.

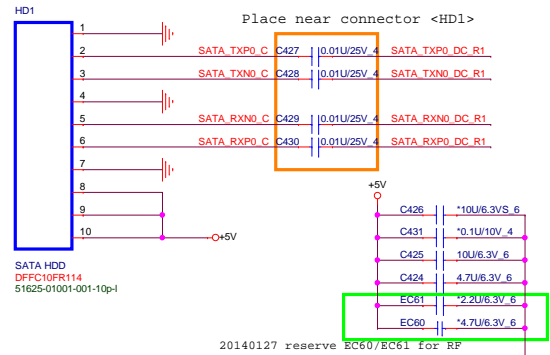


20140122 Stuff keyboard capacitors for EMI

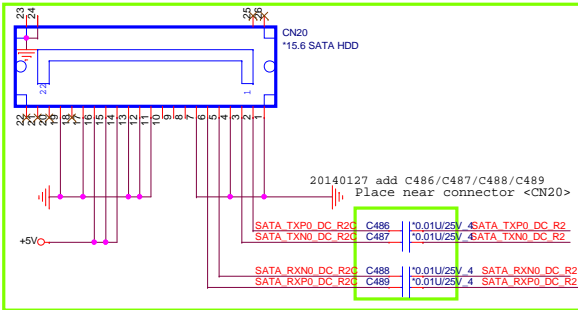
KEYBOARD PULL-UP



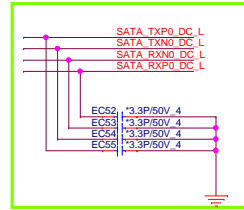
SATA HDD CONNECTOR FFC TYPE



SI-co-lay SATA HDD Connector



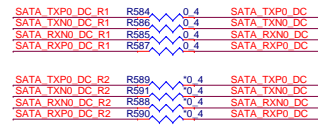
20140123 add for EMI



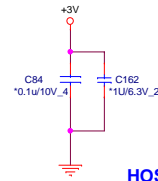
SI add for co-lay use

To FFC conn

To direct conn

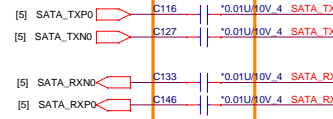


SATA Re-driver



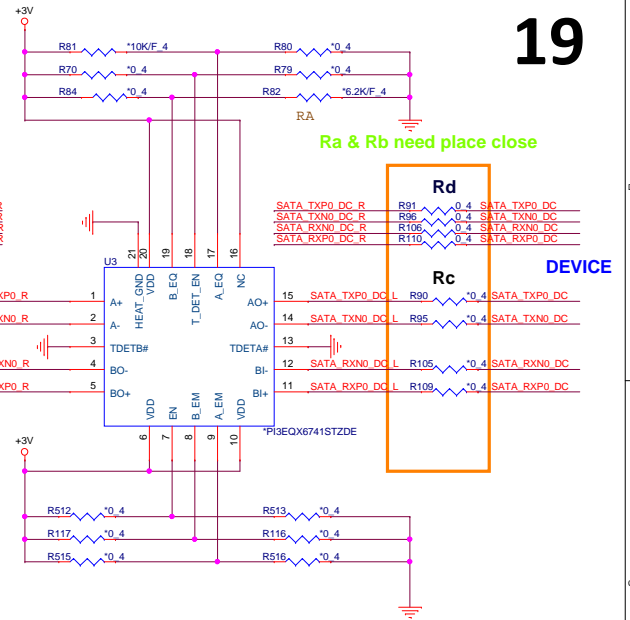
Ra & Rb need place close

HOST



Ra & Rb need place close

DEVICE

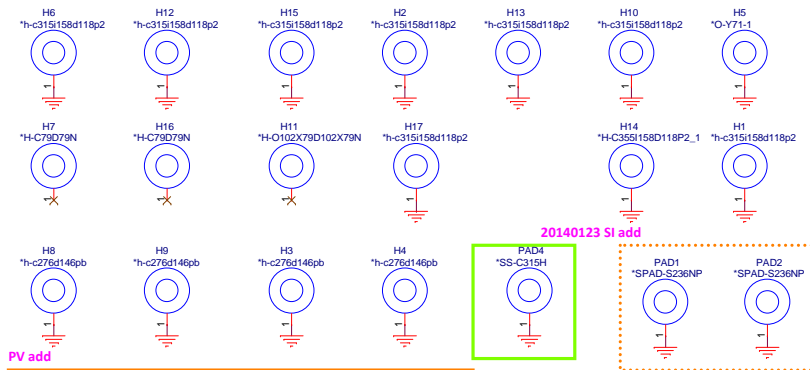


19

SATA re-driver IC
stuff Rb,Rc , unstuff Ra,Rd

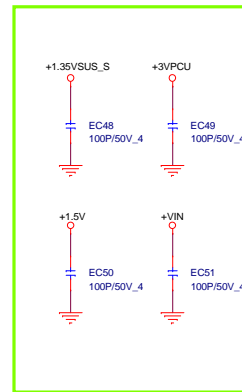
SATA direct
unstuff Ra,Rc , stuff Ra,Rd

HOLE

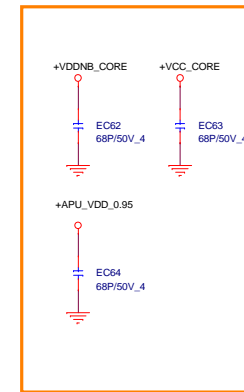


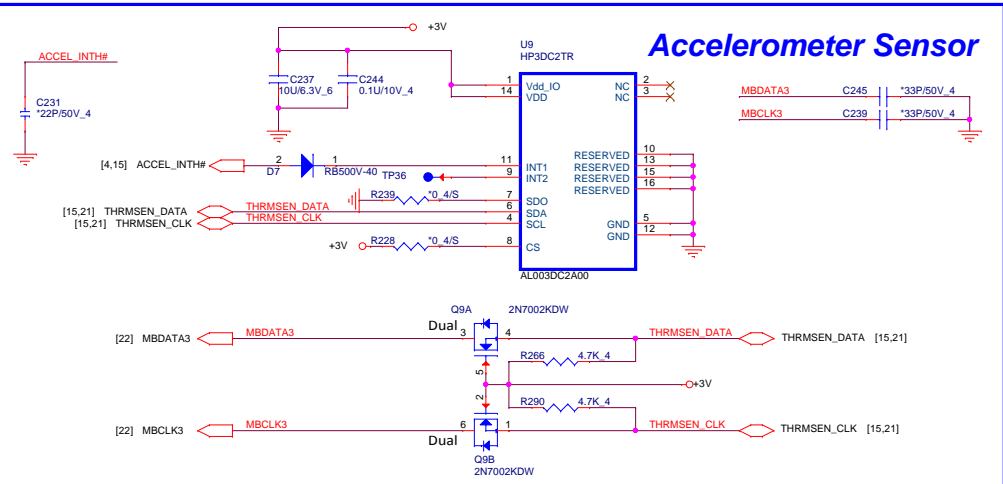
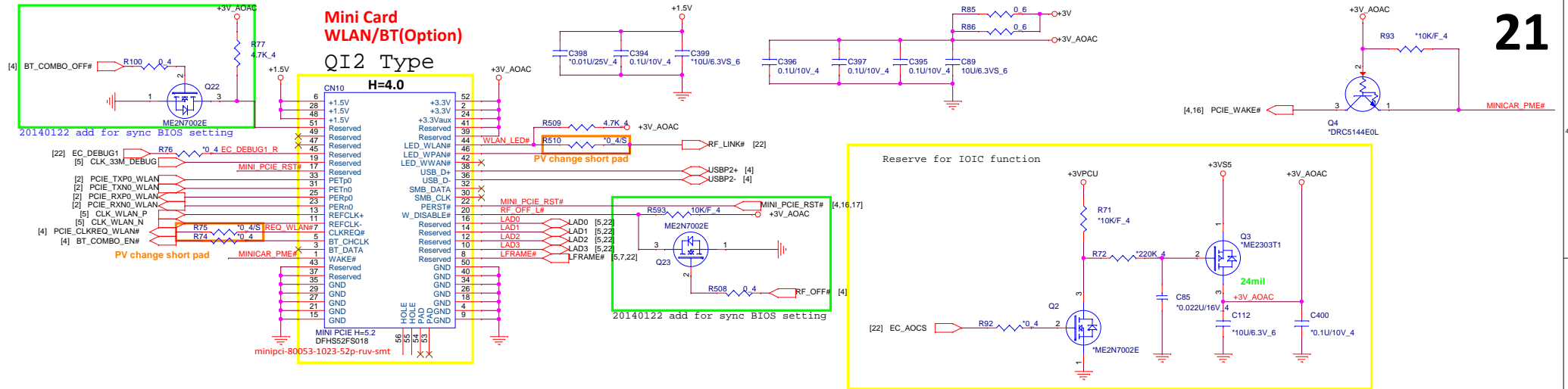
20140123 SI add

20140123 SI add for EMI

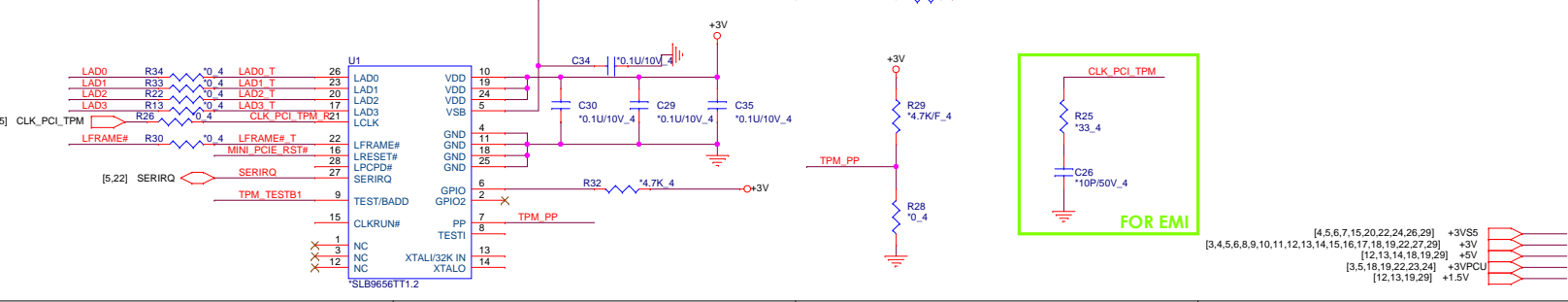



PV add for EMI

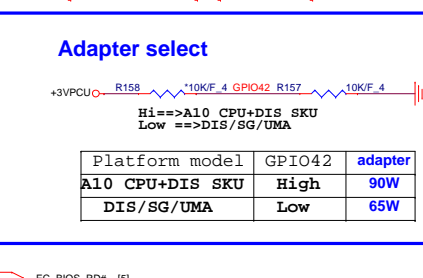
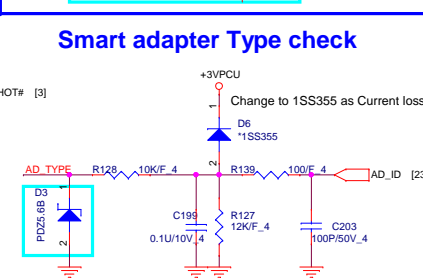
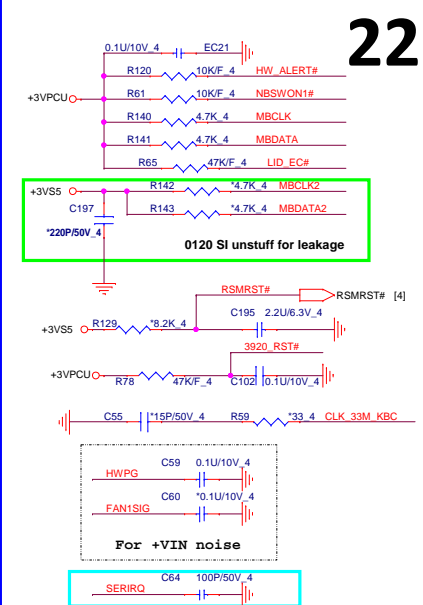
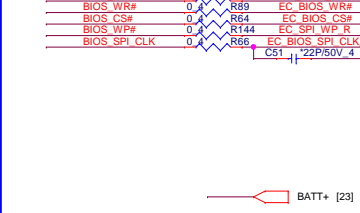
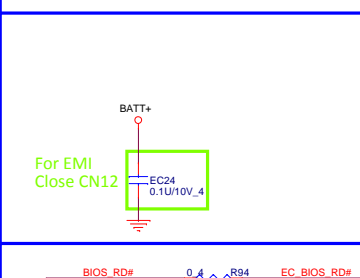
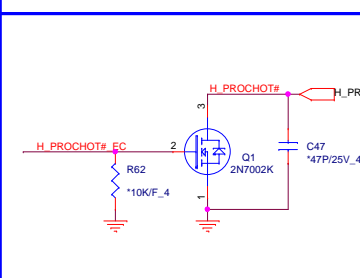
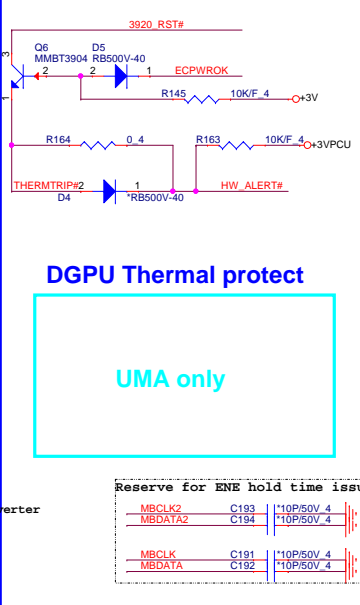
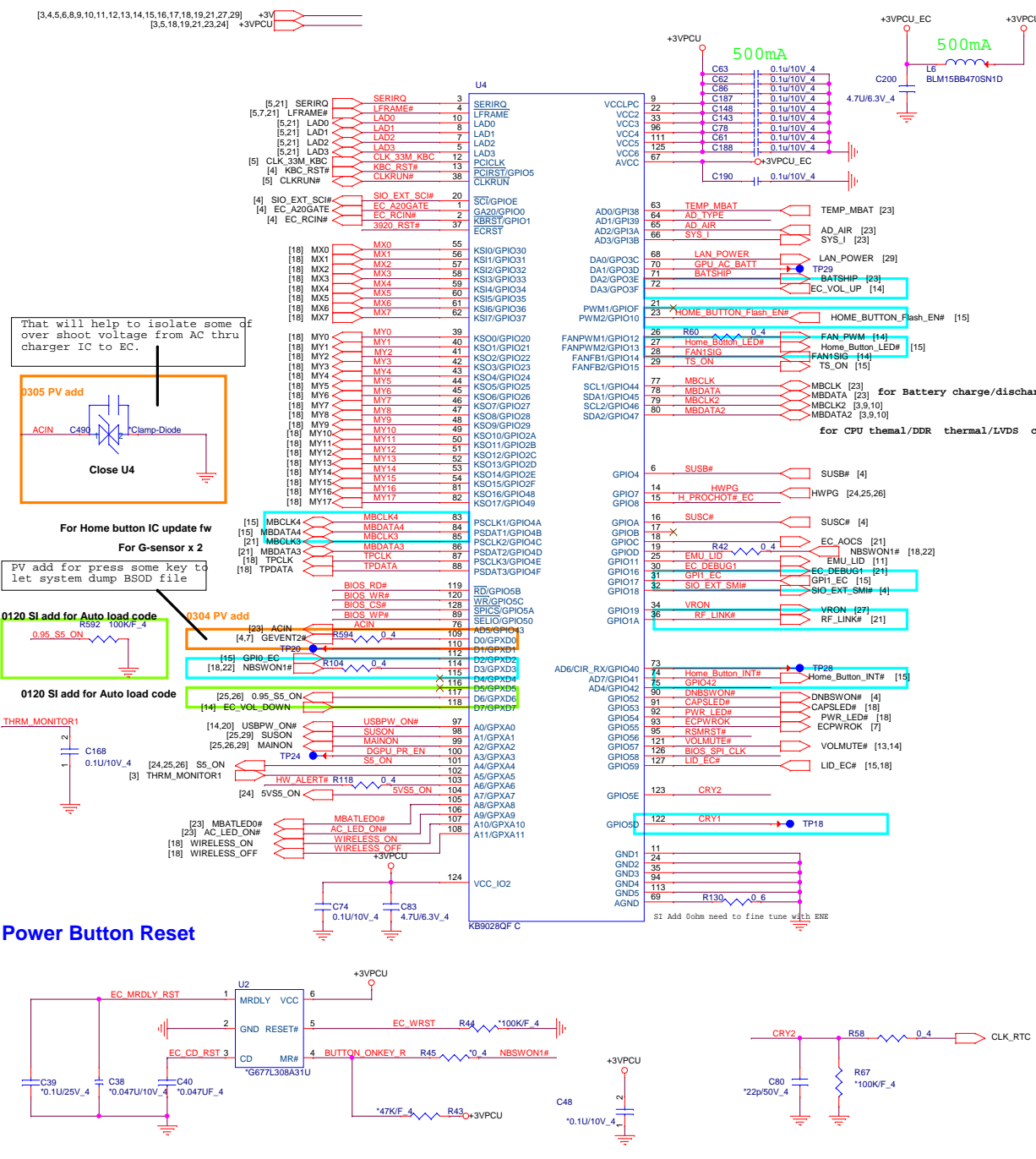




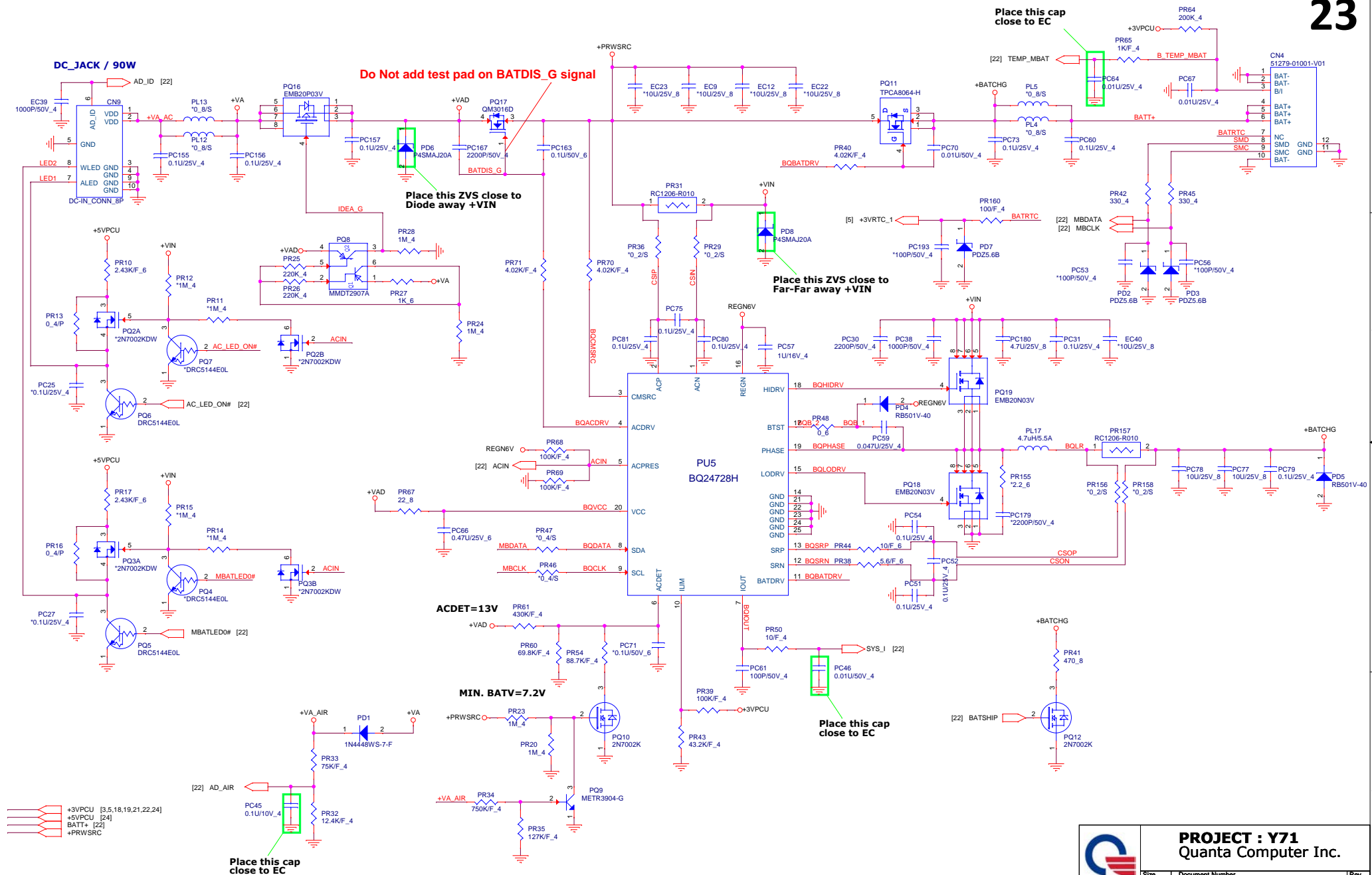
TPM (1.2)

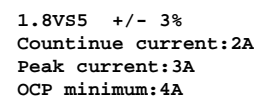
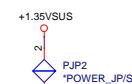


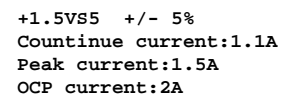
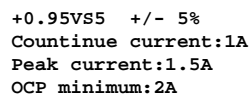
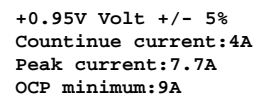
| | | | |
|---|---|--|-----------|
|  | PROJECT : Y71 Quanta Computer Inc. | | |
| | Size Custom | Document Number WLAN/G-Sensor/GCLK/TPM | Rev 1A |
| | Date: Tuesday, March 18, 2014 | Sheet 21 of 30 | |

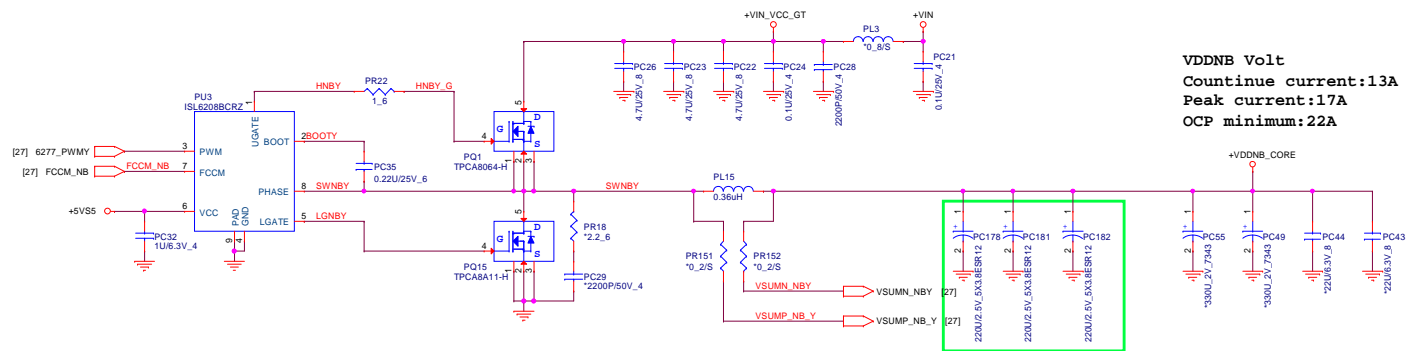
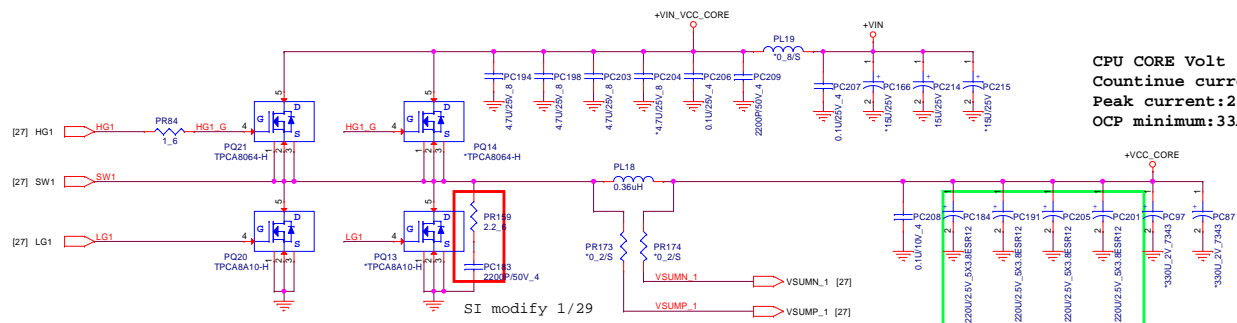


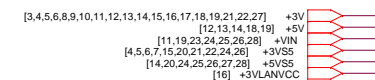
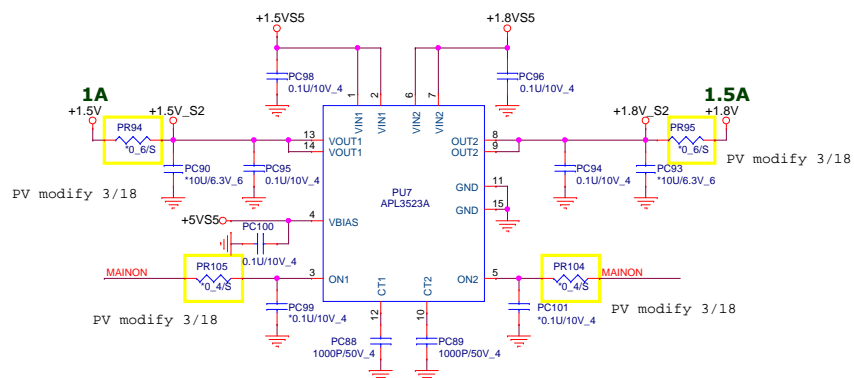
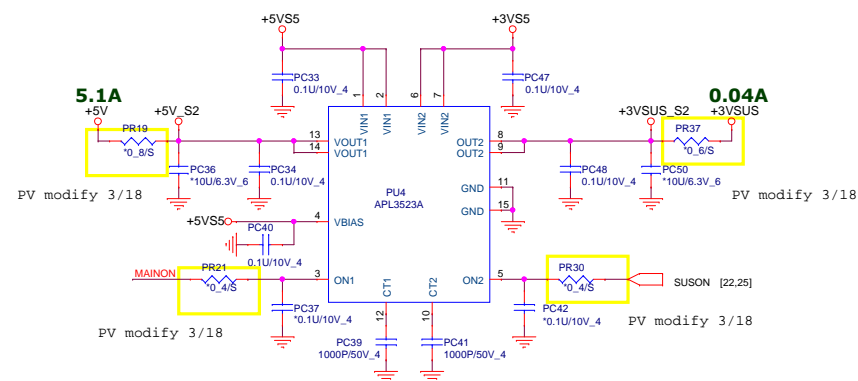
| Platform model | GPI042 | adapter |
|-----------------|--------|---------|
| A10 CPU+DIS SKU | High | 90W |
| DIS/SG/UMA | Low | 65W |











APU Beema

Touchpad

DDR Thermal

RTD2136
DDR Thermal IC

EC KB9028QF

DDR Thermal IC

Battery
Charger

Home Key FW update

G sensor

Touch Pad

